

REMARKS

I. Status of the Application

Claims 12-18 are pending in this application. In the November 30, 2004 office action, the Examiner:

- A. Required labeling of blocks of Fig. 4 of the drawing figures;
- B. Objected to claims 12-18 because of an alleged informality in claim 12.
- C. Objected to the disclosure because of alleged informalities relating to claim 13.

In this response, applicant has amended claim 12, and has submitted a replacement sheet 5/6 having Fig. 4 thereon. Applicant gratefully acknowledges that the Examiner has deemed the case essentially in a condition for allowance. The applicant respectfully requests allowance in view of the foregoing amendments and the following remarks.

II. Figure 4 of the Drawings has been Amended

Applicant has submitted a replacement sheet which contains amendments to Fig. 4 of the drawings. The amendments conform to the Examiner's requirement that the blocks of the drawing figure be labeled. Applicants will submit a formal drawing upon acceptance by the Examiner.

III. The Objections to the Claims are Moot

The Examiner objected to claims 12-18 because of an alleged informality in claim 12. In particular, the Examiner alleged that in claim 12, lines 15, “clock generator” should be replaced by “output signal of the first multiplexer”.

It is respectfully submitted that it is believed that the Examiner meant to require replacement of “clock generator *signal*” with “output signal of the first multiplexer”. As stated earlier in claim 12, the “clock generator signal” is provided as the output signal of the first multiplexer. Accordingly, it is the “clock generator signal” that may suitably be replaced by the phrase “output signal of the first multiplexer”.

Applicant does not believe the change is necessary. However, the Applicant does not object to the requested amendment and therefore has amended the application as required by the Examiner. Accordingly, the objection to the claims is moot.

IV. The Objection to the Disclosure Should be Withdrawn

Applicants respectfully request that the Examiner withdraw the objection to the disclosure. In the objection, the Examiner queried:

where in the specification is the limitation “the clock generator signal is generated by a combination of the received signals of at least two transmission lines of the interfaces, particularly by averaging”, recited in claim 13.

(November 30, 2004 office action at p.3)

It is respectfully submitted that the specification clearly teaches the limitation as originally filed. In particular, the Examiner’s attention is drawn to page 6, line 27 to page 7, line 5, which teaches the above limitation. The above limitation is also supported by a non-limiting example described at page 9, line 36 to page 10, line 1. It is

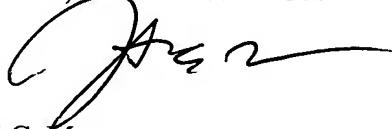
respectfully submitted that no further amendment of the specification is needed to support or adequately describe the limitation of claim 13, cited by the Examiner.

V. Conclusion

For all of the foregoing reasons, it is respectfully submitted that the application is in a condition for allowance. Favorable reconsideration and allowance of this application is, therefore, earnestly solicited.

Respectfully Submitted,

MAGINOT, MOORE & BECK



January 31, 2005

Harold C. Moore
Registration No. 37,892
Bank One Center/Tower
111 Monument Circle, Suite 3000
Indianapolis, Indiana 46204-5115

Enclosures